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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet **1** of **2**

Application Number	10/729,970
Filing Date	9 December 2003
First Named Inventor	LIU, Yan-Fei
Art Unit	2838
Examiner Name	BERHANE, Adolf D.
Attorney Docket Number	14546

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
AB		US- 4,695,933	09-22-1987	Nguyen, et al.	
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FOREIGN PATENT DOCUMENTS						
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Examiner Signature	A. Berhane	Date Considered	7/28/05
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Sheet 2

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Attorney Docket Number	14546

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AB		JIA WEI, PENG XU, and FRED C. LEE, A High Efficiency Topology for 12V VRM - Push-Pull Buck and Its Integrated Magnetics Implementations, Virginia Polytechnic Institute and State University, 2002, pp. 679-685, IEEE: Blacksburg, VA, USA.	
AB		PENG XU, JIA WEI, KAIWEI YAO, YU MENG, and FRED C. LEE, Investigation of Candidate Topologies for 12V VRM, Virginia Polytechnic Institute and State University, 2002, pp. 686-692, IEEE: Blacksburg, VA, USA.	
AB		YAN-FEI LIU, and PARESH C. SEN, New Class-E DC-DC Converter Topologies with Constant Switching Frequency, IEEE Transactions on Industry Applications, Vol. 32, No. 4, July/August 1996, pp.961-969, IEEE, USA.	
AB		YAN-FEI LIU, and PARESH C. SEN, A Novel Resonant Converter Topology for DC-to-DC Power Supply, IEEE Transactions on Aerospace and Electronic Systems, Vol. 31, No. 4, October, 1995, pp. 1301-1313, IEEE, USA.	
AB		YAN-FEI LIU, and PARESH C. SEN, Source Reactance Lossless Switch (SRLS) for Soft-Switching Converters with Constant Switching Frequency, IEEE Transactions on Circuits and Systems - I: Fundamental Theory and Applications, Vol. 43, No. 4, April 1996, pp. 301-312, IEEE, USA.	

Examiner Signature	<i>A. Berhane</i>	Date Considered	7/28/05
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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